Abstracts

High-Speed GaAs Dynamic Frequency Divider Using a Double-Loop Structure and Differential Amplifiers (Short Papers)

M. Shigaki, T. Saito, H. Kusakawa and H. Kurihara. "High-Speed GaAs Dynamic Frequency Divider Using a Double-Loop Structure and Differential Amplifiers (Short Papers)." 1988 Transactions on Microwave Theory and Techniques 36.4 (Apr. 1988 [T-MTT]): 772-774.

New GaAs 2.0-8.0 GHz and 6.0-10.5 GHz dynamic frequency dividers have been developed. These dynamic dividers have a double-loop structure using a differential amplifier for high-speed and stable operation despite supply voltage fluctuations. This structure operates from one voltage supply. An advanced WSi self-aligned gate process technology (1.0 µm long gate) was used to improve the high-frequency characteristics of the FET.

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